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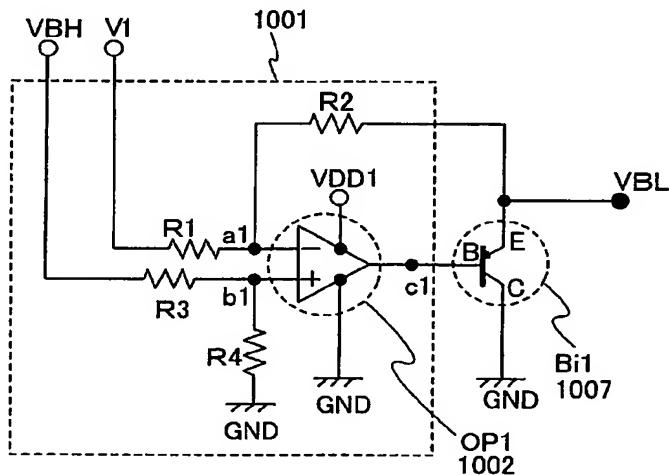
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(54) Title: LIGHT EMITTING DEVICE



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(57) Abstract: Power consumption required for charging and discharging a source signal line is reduced in an active matrix EL display device. A bipolar transistor (BiT) has a base terminal B connected to an output terminal c1 of an operational amplifier (OPI), a collector terminal C connected to a low power potential (GND), and an emitter terminal E connected to a resistor R2. A high power potential (VBH) is a potential in synchronization with a high power potential of a light emitting element. A potential of the output terminal c1 of the operational amplifier (OPI) is outputted as a buffer low power potential (VBL). The low power potential (VBL) corresponds to a potential difference between the high power potential (VBH) and a high power potential (VI). Accordingly, the low power potential (VBL) can follow the high power potential (VBH), that is a high power potential of the light emitting element.